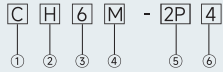


CH6M/S Timing counter



Product selection



Item	Code	Description
① Company name	C	CNTD
② Series	H	Counter/timer
③ Installation dimension	6	999999
④ Dimensions	S	DIN W48×H48mm
	M	DIN W72×H72mm
⑤ Output	1P	1 stage setting type
	2P	2-stage setting type
⑥ Supply voltage	2	24VAC 50/60Hz 24-48VDC
	4	100-240VAC 50/60Hz

Application

The timing counter can be used in industrial control occasions. It has two working modes, timing work and counting, and the corresponding functions can be selected according to the use scenario.

Features

- One-shot output time, in units of 10ms, can be set from 0.01 seconds to 99.99 seconds;
- Counter mode
Display range 0.00001-999999;
9 input modes and 11 output modes to choose from;
BATCH counting, counting initial value setting function;
Optional up to 10kcps input speed;
- Timer mode
12 time range formats are available;
13 output modes are available;
Wide time setting range (0.001 seconds-99999.9 hours)
Support forward timing and countdown;
- Power-off memory function;

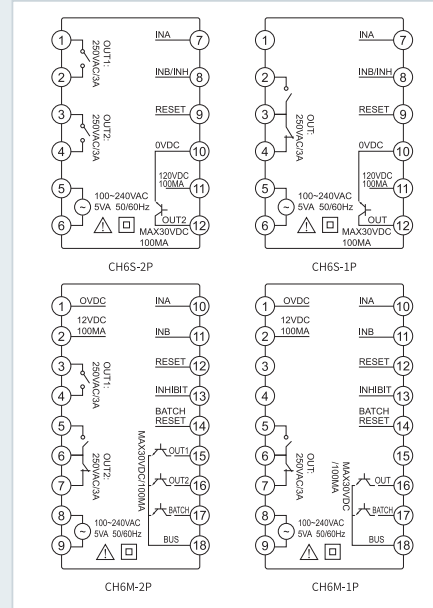
Electrical Specification

Voltage	AC power supply type	100-240VAC 50/60Hz
	DC power supply type	24VDC
Power consumption		≤ 12VA
INA/INB maximum counting speed		1cps/30cps/1Kcps/5Kcps/10Kcps can be selected
Minimum signal width	Counter	Reset input: 1ms, 20ms optional
	Timer	NA, RESET, INHIBIT, BATCH RESET reset signal: 2ms, 20ms optional
Input method		No voltage input
One-shot output time	Counter, timer, 0.01seconds-99.99 seconds	
Memory storage		10 years (non-volatile semiconductor memory)
Wiring method		Terminal block
Use the surrounding humidity		-25-+70°C (not frozen state)
Storage temperature		-30-+80°C (not frozen state)
Humidity		35%-95%RH(Non-condensing state)

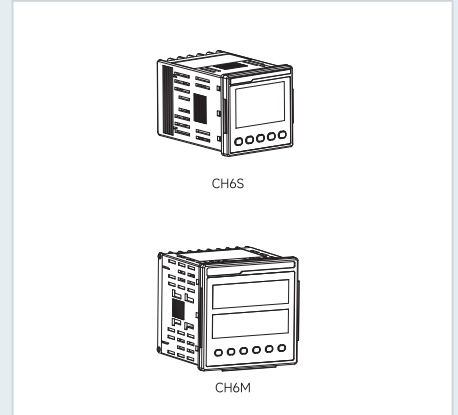
CH6M/S Timing counter



Wiring diagram



Dimensions



Model number	Panel size	Shell size (length×width×height)	Opening size
CH6S	48×48	44.3×44.3×81	45×45
CH6M	72×72	67×67×81	68×68

Input mode (counter)

Input mode	Timing diagram	Input mode	Timing diagram
UP (Up)		dn-2 (Down-2)	
UP-1 (Up-1)		Ud-A (Up/Down-A)	
UP-2 (Up-2)		Ud-b (Up/Down-B)	
dn (Down)		Ud-C (Up/Down-C)	
dn-1 (Down-1)			

CH6M/S Timing counter



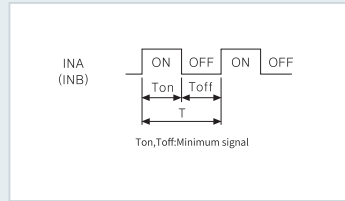
※ Ⓐ is above the minimum signal width and Ⓑ is above 1/2 of the minimum signal width. When it is less than this signal width, a counting error of ±1 may occur.

※The meanings of "H" and "L" in the table below are as follows

	(PNP)	(NPN)
H	5-30VDC	(Short)
L	0-2VDC	(Open)

※The minimum signal width of each counting speed

Counting speed	Minimum signal width
1cps	500ms
30cps	16.7ms
1kcps	0.5ms
5kcps	0.1ms
10kcps	0.05ms



Output mode (counter)

Output mode	Timing diagram			Output mode	Timing diagram		
	Up, Up-1, 2	Down, Down-1, 2	Up/Down A,B,C		Up, Up-1, 2	Down, Down-1, 2	Up/Down A,B,C
F (F)				K (K)			
n (N)				ε (K)			
ζ (C)				q (Q)			
Γ (R)				Ρ (A)			

※The OUT output in the 1-stage setting type is the same as the OUT2 output in the 2-stage setting type.
 ※The OUT1 output can be set to 0 in all output modes, and the corresponding value can be output.
 ※When the output mode is C (c),R(r),P(p),Q(q), the OUT2 output cannot be set to 0.

Output mode (counter)

Output mode	Up/Down -A,B,C
5 (S)	
t (T)	
d (D)	

※The OUT output in the 1-stage setting type is the same as the OUT2 output in the 2-stage setting type.
 ※In the 2-stage setting type, OUT1 can choose one-shot output or Hold output mode.
 ※OUT1 In all output modes, the preset value can be set to 0, and the output is consistent with the state corresponding to the preset value of 0.
 ※When the output mode is C (c),R(r),P(p),Q(q), the OUT2 output cannot be set to 0.

CH6M/S Timing counter

Output mode (timer)

Output mode	Timing diagram	Output mode	Timing diagram
ond (OND)		ond2 (OND.2)	
ond.1 (OND.1)		FLK (FLK)	
FLK.1 (FLK.1)		FLK2 (FLK.2)	
int (INT)		int2 (INT.2)	
int.1 (INT.1)		nFd.1 (NFD.1)	
ofd (OFD)		intG (INTG)	
nFd (NFD)			